Decoupling capacitors to suppress supply noise.
* Place near op-amp.

Single-ended to differential output amplifier with a gain of 2.

Anti-aliasing filters on differential outputs, (fC = 79.5KHz)
Note that these bypass resistors are optional for bypassing the amplification stage. Do not populate them if the amplifier is not bypassed as they can result in an antenna on the PCB.

If amplification is bypassed, unpopulate the resistor on the amplification stage output.

First-order lowpass filter (fc = 50.4kHz)

First-order highpass filter (fc = 15.9kHz)

Supply decoupling capacitors to suppress supply noise.
* Place close to IC.

Dual Channel Digital Potentiometer
Used for digitally-controlled signal gain.
Note that these bypass resistors are optional for bypassing the amplification stage. Do not populate them if the amplifier is not bypassed as they can result in an antenna or the PCB.

If amplification is bypassed, unpopulate the resistor on the amplification stage output.

First-order lowpass filter (fc = 50.4kHz)

First-order highpass filter (fc = 15.5kHz)

Supply decoupling capacitors to suppress supply noise.
* Place close to IC.

Dual Channel Digital Potentiometer
Used for digitally-controlled signal gain.
Decoupling capacitors to suppress supply noise.

* Place near op-amp.

Single-ended to differential output amplifier with a gain of 2.

Anti-aliasing filters on differential outputs.

(FC = 79.5KHz)
Note that these bypass resistors are optional for bypassing the amplification stage. Do not populate them if the amplifier is not bypassed as they can result in an artifact on the PCB.

If amplification is bypassed, unpopulate the resistor on the amplification stage output.

First-order lowpass filter (fc = 50.4kHz)

First-order highpass filter (fc = 15.9kHz)

Supply decoupling capacitors to suppress supply noise.
* Place close to IC.

Dual Channel Digital Potentiometer
Used for digitally-controlled signal gain.
Note that these bypass resistors are optional for bypassing the amplification stage. Do not populate them if the amplifier is not bypassed as they can result in an ozonae on the PCBA.

If amplification is bypassed, unpopulate the resistor on the amplification stage output.

First-order lowpass filter (fc = 50.4kHz)

First-order highpass filter (fc = 15.8kHz)

Supply decoupling capacitors to suppress supply noise.
* Place close to IC.

Dual Channel Digital Potentiometer
Used for digitally-controlled signal gain.